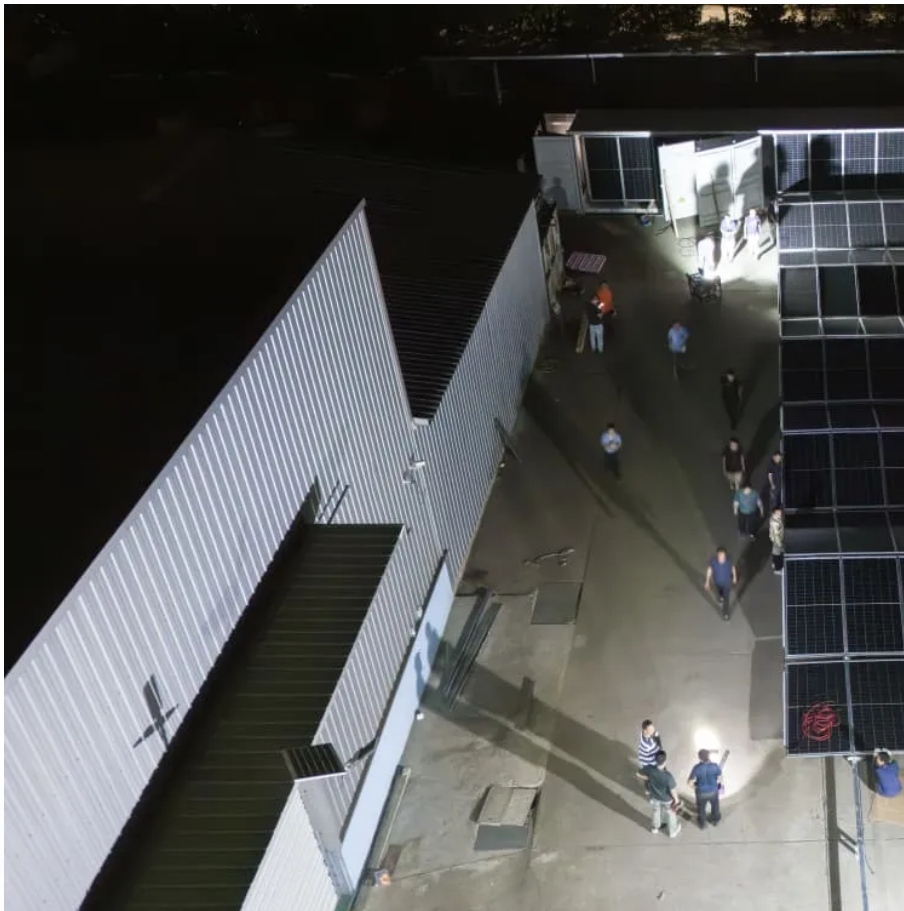


# Inverter design high frequency





## Overview

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What is a high frequency inverter?

In many applications, it is important for an inverter to be lightweight and of a relatively small size. This can be achieved by using a High-Frequency Inverter that involves an isolated DC-DC stage (Voltage Fed Push-Pull/Full Bridge) and the DC-AC section, which provides the AC output.

What is a high frequency variable load inverter architecture?

This thesis presents a high frequency variable load inverter architecture along with a physical prototype and efficiency optimizing controller. The inverter architecture consists of two constituent inverters, one connected directly through the load and the other connected through an immittance converter, which acts as a lossless power combiner.

Can a high-frequency variable load inverter directly drive widely variable loads?

Typically a tunable matching network is used to transform the varying load into a efficiency and impairing transient response. This thesis presents the design, physical prototype, controller, and experimental results of a high-frequency variable load inverter architecture (referred to as HFVLI) that can directly drive widely variable loads.

What are the operating principles of a high-frequency PDM inverter?

The inverter's steady-state and transient operating principles are analyzed based on the proposed control strategy at a high-frequency PDM. Operating conditions such as power loss analysis, power conversion efficiency and temperature rise characteristics of the proposed inverter are presented and analyzed through experimental results.



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