

Three-phase inverter voltage dual-loop control





Overview

How is a three-phase PV Grid-connected inverter designed?

The three-phase PV grid-connected inverter was designed based on the LQR method, where the tracking error was adjusted to zero through integration (Al-Abri et al., 2024). The disturbance rejection ability of the PV GCI was improved by designing the linear state inaccuracy feedback control policy (Zhou et al., 2021).

What is a phase-locked loop (PLL) in a voltage source inverter?

The primary cascaded control loops and the phase-locked loop (PLL) can enable voltage source inverter operation in grid-forming and grid-following mode.

How does a three-phase inverter work?

In this test case, STS is open ($x_{STS} = 0$) and the inverter caters to the power demand from the three-phase load. The three-phase loads are configured to operate in constant power mode with the current limit of 8 A. Measured data from the spectrum analyser are fetched and plotted for controller performance analysis.

How is a three phase voltage transformed into a B C - D Q?

Measured three-phase voltage and currents are transformed into a d q frame signals based on the grid voltage angle or inverter internal voltage angle provided by the PLL. To obtain the linear representation of dynamics of SRF-PLL and its influence on a b c - d q transformation, the small-signal technique is generally considered .



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